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ENCLOSURE FOR COMPUTER  
PERIPHERAL DEVICES

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1        This is a continuation in part of the application filed  
2        under Serial No. 09/310,036 on May 11, 1999.

3        BACKGROUND OF THE INVENTION

4        This invention relates to enclosures for receiving and  
5        operating computer peripheral devices and more particularly  
6        to enclosures that allow computer peripheral devices, such  
7        as hard disk drives, to be readily interchanged and used in  
8        different combinations.

9        Computers include storage devices and other peripheral  
10       devices. These storage devices include compact disk drives,  
11       floppy disk drives, magnetic optical disk drives and tape  
12       back-up systems. These devices are connected by wires and  
13       are operated in conjunction with a computer. The spectacular  
14       proliferation of electronic devices, particularly computers,

1 in modern society, both in numbers and complexity, demands  
2 that such devices satisfy ever-increasing standards of  
3 reliability and serviceability to avoid degeneration into  
4 chaos. In the early days of the computer industry,  
5 relatively high failure rates and corresponding "down time"  
6 when the computer system was unavailable to perform useful  
7 work were accepted as the norm. As the industry has  
8 matured, computers have become more reliable, and users have  
9 come to depend on these systems being available when needed.  
10 This dependence has become so profound that, for many  
11 businesses, the mere unavailability of the computer system  
12 for any appreciable length of time can cause significant  
13 commercial injury.

14 In the early days of the computer industry, a computer  
15 component was replaced by shutting off power to the system,  
16 replacing the component, and re-powering the system. This  
17 is a logical way to fix a toaster, but the complexity of  
18 modern computers makes this undesirable. It is not possible

1 to simply shut off power and then turn it back on as one  
 2 would a light bulb. A computer system's state and data must  
 3 be saved when it is powered down. Its software must be re-  
 4 loaded, and its state restored, when it is re-powered. For  
 5 a large modern computer system, these operations can take a  
 6 very significant amount of time, during which the system is  
 7 unavailable to its customers.

8 Computer manufacturers are well aware of the dependence  
 9 of their customers and have accordingly devoted considerable  
 10 attention to these problems. As a result, many modern  
 11 computer systems have some degree of fault tolerance and are  
 12 capable of concurrent maintenance. Fault tolerance means  
 13 simply that a single component of the computer system may  
 14 fail without bringing the entire system down, although in  
 15 some cases performance of the system or some other  
 16 characteristic may be adversely affected. Concurrent  
 17 maintenance is the capability to repair or replace some  
 18 component of a computer system without shutting down the

1 entire system. The system can continue to operate and  
 2 perform useful work (although possibly in a diminished  
 3 capacity) while the repair is being performed. When a  
 4 computer system is both fault-tolerant and capable of  
 5 concurrent maintenance it can, in theory, be kept running 24  
 6 hours a day for an indefinite length of time. In fact few,  
 7 if any, computer systems achieve this level of reliability  
 8 with respect to every component which may possibly fail.

9 One example of this type of fault tolerance is an array  
 10 of storage devices known as a redundant array of independent  
 11 disks. A RAID system stores data on multiple storage  
 12 devices in a redundant fashion, such that any data can be  
 13 recovered in the event of failure of any single storage  
 14 device in the array. RAID systems are usually constructed  
 15 with rotating magnetic hard disk drive storage devices, but  
 16 may be constructed with other storage devices such as  
 17 optical drives, fiber channel devices and tape drives. A  
 18 paper entitled, "A Case for Redundant Arrays of Inexpensive

1 Disks", by Patterson, Gibson & Katz and presented at the ACM  
 2 SIGMOD conference (June, 1988) describes RAID systems that  
 3 provide different levels of redundancy or other operating  
 4 characteristics and classifies five types of RAID,  
 5 designated levels 1 through 5. The Patterson Nomenclature  
 6 has become standard in the industry. RAID systems have  
 7 proliferated to the point where an industry trade group  
 8 called the RAID Advisory Board has attempted to establish  
 9 standards for RAID characteristics. Further information  
 10 regarding RAID systems can be found in The RAID-book, A  
 11 Source Book for Disk Array Technology, published by the RAID  
 12 Advisory Board (5th Ed. February 1996).

13 Frequently, a RAID system is manufactured and marketed  
 14 as a stand-alone storage system that is housed in its own  
 15 cabinet with its own power supply and supporting hardware  
 16 and software and that communicates through a standard  
 17 communications interface with a host computer system. Since  
 18 it is desirable to make data available to the host system at

1 all times, even if a single storage device in the RAID  
 2 system fails, the RAID system will frequently have its own  
 3 on-board data recovery capability that may include temporary  
 4 spare drives for storage of recovered data. The RAID system  
 5 may have redundant power supplies or other redundant  
 6 components.

7       Electronic systems frequently use back-plane circuit  
 8 cards for distribution of power, data signals and/or  
 9 mounting of active or passive circuit elements and  
 10 connectors. Such a card typically contains multiple  
 11 parallel layers for embedded circuit patterns, grounds, or  
 12 power distribution. Pluggable connectors couple the back-  
 13 plane to other modules which make up the electronic system,  
 14 such as power supply modules, storage devices, or logic  
 15 cards. A back-plane card acts primarily as a distribution  
 16 medium. The back-plane card conveys power and/or data  
 17 signals from one module to another and contains relatively  
 18 few functional components attached directly to the back-

1 plane itself. The back-plane contains functional  
2 components.

3 A back plane contains embedded power planes for  
4 distributing electrical power to pluggable modules or other  
5 attached devices. It also contains a multiple ground  
6 planes. One of the power planes is associated with ground  
7 and must be grounded. Grounding insures that electrical  
8 paths of negligible resistance are created between ground  
9 planes and the zero volts power plane.

10 The existence of such paths causes parasitic current to  
11 flow in the ground planes and lowers the noise immunity of  
12 the system. It is desirable to reduce the parasitic current  
13 flowing in the ground planes of a back-plane circuit card,  
14 particularly a back plane that distributes power to multiple  
15 pluggable modules.

16 U. S. Patent No. 4,977,532 teaches a computer that  
17 includes computer cards, disk drives and power supply  
18 circuitry that are mounted in a drawer assembly that is

1 readily removable from the rear of the cabinet.

2 U. S. Patent No. 6,025,989 teaches a rack mounted  
3 multiprocessor in a computer that has a node assembly. The  
4 node assembly has a logic chassis and a removable chassis.  
5 The logic chassis contains logic cards that include memory  
6 cards, a service processor card, central processing unit  
7 processor cards and input/output cards to which input/output  
8 and processor interconnecting cables are attached. The  
9 removable chassis contains the power supply module for the  
10 node, a node supervisor card, disk drives and cooling fans.  
11 The removable chassis is removable from the logic chassis  
12 without moving or disturbing the logic chassis. One fan of  
13 a pair of cooling fans in the removable chassis draws air  
14 through the power supply that has relatively higher cooling  
15 needs and blows the air into the logic chassis over logic  
16 modules that have relatively lower cooling needs. The other  
17 cooling fan draws air over the disk drives and node  
18 supervisor card and blows air over into the logic chassis



1 over logic modules that have relatively higher cooling  
2 needs.

3 U. S. Patent No. 5,604,873 teaches a disk drive  
4 controller that has a plurality of disk drive interfaces.  
5 Each disk drive interface includes a connector and a delay  
6 circuit. A set of power application circuits has a server  
7 which supports hot docking of SCA drives. Each connector  
8 mates with a hot docking disk drive having equal length  
9 connecting pins and detects the presence of such disk drive  
10 when the hot docking disk drive makes contact with the  
11 connector. Each delay circuit generates a set of properly  
12 delayed enabling signals to the corresponding power  
13 application circuits. Each set of power application  
14 circuits regulates power applications to the hot docking  
15 disk drive making contact with the corresponding connector.  
16 The delayed and regulated manner of applying power prevents  
17 voltage and power swings that might disrupt on-going  
18 operations and/or cause damages to the neighboring drives.

1 U. S. Patent No. 5,790,374 teaches a computer cabinet  
2 and hot-pluggable disk drive module design that includes an  
3 indicator light support for the disk drive module. The  
4 cabinet includes a disk drive module-receiving bay into  
5 which the disk drive module is removably installed. The  
6 disk drive receiving module bay includes a back plane having  
7 at least one connector for engagement with a corresponding  
8 connector that is a single connector architecture connector,  
9 protruding from a leading surface of the disk drive module.  
10 A light source mounted to the back plane near the back plane  
11 connector is illuminated to provide status information  
12 concerning the disk drive module. A light conduit extends  
13 from the light source to a conspicuous viewing location on  
14 the computer cabinet. Multiple indicator lights and  
15 corresponding light conduits are tinted to different colors  
16 and may be employed to provide different status indications,  
17 such as power, activity and fault status information. The  
18 light conduits are incorporated into the disk drive modules.

1 Once the disk drive module has been installed within the  
 2 drive bay the light conduits extend from a location on a  
 3 leading surface of disk drive tray opposing the light  
 4 sources contained on the back plane to a conspicuous viewing  
 5 location on a visible surface of the disk drive module.

6 U. S. Patent No. 5,862,313 teaches a system that  
 7 implements a serial RAID system. Data is striped for an  
 8 array of disk drives and parity for the striped data is  
 9 calculated and the resulting data and is written serially to  
 10 a RAID system over a Fiber Channel or other type of network.  
 11 The system also allows reading of the striped data and  
 12 parity serially from the disk array.

13 U. S. Patent No. 5,812,754 teaches a modular and highly  
 14 available RAID system that has a fiber channel arbitrated  
 15 loop interface coupled with a disk array. Fault-tolerant  
 16 operation is assured. The system provides dual and isolated  
 17 arbitrated host and storage device loop circuits for  
 18 redundant, independent input/output paths to local and/or

1 remote host computers. Each loop includes bypass circuits.  
2 The bypass circuits prevent the failure of any device (host  
3 computer or storage device) from affecting the operation of  
4 loop. Orthogonal data striping may be used to further  
5 assure data integrity.

6 U. S. Patent No. 5,805,788 teaches a system that  
7 implements RAID-5 parity generation and reconstruction.  
8 Data for an array of disk drives is placed in an  
9 input/output buffer. The RAID-5 parity engine creates  
10 parity data and stores the resulting parity data in the  
11 input/output buffer as well. The input/output buffer (both  
12 the data and the parity) is then sector-striped across a  
13 network of disk drives, such as a Fiber Channel network.  
14 The RAID-5 parity engine creates parity on multiple stripes  
15 of data upon a single activation of the engine. The RAID-5  
16 parity engine can reconstruct a sector of data using the  
17 parity information. The RAID-5 parity engine can also check  
18 the data against its expected parity.

1 U. S. Patent No. 5,740,397 teaches an adapter for use  
 2 in a computer system. The adapter has an IDE interface  
 3 controller and a plurality of drives. The adapter is  
 4 connected to the IDE controller and the plurality of drives  
 5 and monitors information transmitted between the IDE  
 6 interface controller and the plurality of drives. The  
 7 adapter determines whether each of the plurality of drives  
 8 is serviceable and whether data on each of the plurality of  
 9 drives are equal. Identical data is directed to each of the  
 10 plurality of drives when data on each of the plurality of  
 11 drives are equal. Data is prevented from being directed to  
 12 at least one of the plurality of drives when data on each of  
 13 the plurality of drives are not equal. One of the  
 14 pluralities of drives is selected as a drive from which to  
 15 read data.

16 U. S. Patent No. 5,515,515 teaches a small computer  
 17 system interface disk array subsystem that has removable  
 18 disk drive units that can be removed while the subsystem has

1 power applied to it and is operational. In order to prevent  
2 malfunctions of other disks in the disk array, and in order  
3 to prevent rapid fluctuations in the current in the power  
4 circuits of the subsystem, the power and the data and  
5 control signals are applied to the inserted disk drive unit  
6 in a phased sequence. The ground of the disk drive unit is  
7 connected to the ground of the subsystem. A pre-charge  
8 power connects the power buses of the disk drive unit to the  
9 power buses of the subsystem through resistors so that  
10 capacitance is connected to the power buses of the disk  
11 drive unit is charged gradually through the resistors rather  
12 than abruptly. Power is applied directly from the power  
13 buses of the subsystem to the power buses of the disk drive  
14 unit. Finally, the control and data signal connections are  
15 made between the subsystem and the disk drive unit. This  
16 phased sequence of connections between the subsystem and the  
17 disk drive unit is accomplished by providing sets of  
18 contacts having differing lengths in a connector on the disk

1 drive unit so that some connections are made before other  
2 connections.

3 U. S. Patent No. 5,438,226 teaches an enclosure for  
4 heat-generating electronic components that establishes an  
5 airflow path for cooling air. Powered air movers force air  
6 through the enclosure. One powered air mover is mounted at  
7 the air inlet to the enclosure, and the other of which is  
8 mounted at the outlet. Only one air mover is used at a  
9 time. In the event of failure of the air mover being used,  
10 the remaining air mover begins operation. The total cooling  
11 airflow passes through both air movers. The air movers may  
12 be arranged in series with respect to the airflow. Because  
13 the air movers are mounted in series with respect to the  
14 airflow, the airflow pattern is substantially the same  
15 regardless of which air mover is currently operating. Hot  
16 spots may result when one fan in a fan bank fails. The  
17 enclosure may house a redundant array of independent disks  
18 of a computer system. The air movers are arranged serially

1 with the airflow and may be used to provide additional  
2 cooling for severe operating conditions.

3 U. S. Patent No. 5,991,852 teaches a memory system that  
4 includes a main memory such as a cache memory and a shadow  
5 or back-up cache memory in conjunction with a write cache.  
6 The shadow memory is coupled to the same data bus as the  
7 main memory and is written to simultaneously. There is no  
8 latency between writing to the main memory and writing to  
9 the shadow memory. Redundancy is provided for by having a  
10 switching circuit that allows control of the shadow memory  
11 to be transferred to a second controller upon failure of a  
12 first controller. A unique layout arrangement for a RAID  
13 chassis is also described in which back-to-back circuit  
14 boards are mounted in the center of the chassis and a main  
15 bus on one board becomes the shadow bus on the other board,  
16 providing a mirror arrangement for the circuit boards.

17 U. S. Patent No. 6,058,019 teaches an electronic system  
18 that contains a back-plane circuit card assembly for



1 distribution of electrical signals among component devices.  
2 Power is distributed in embedded power planes. One plane is  
3 associated with ground. The back plane also contains  
4 multiple embedded ground planes. The circuit card assembly  
5 is divided by an axis into two halves, each half receiving  
6 and consuming approximately equal power. Electrical  
7 couplings for power sources and power planes in each half of  
8 the back plane are located symmetrically with respect to the  
9 axis. A row of ground vias along the axis couples the  
10 ground planes to the associated power plane. At all other  
11 locations on the back plane the ground planes are  
12 electrically isolated from the associated power plane. The  
13 back-plane assembly includes a pair of base cards, connected  
14 by a single smaller jumper card in which all ground vias are  
15 located. Each base card lies on a respective side of the  
16 axis, the jumper straddling the axis. An intelligent  
17 redundant array of independent disks storage server has  
18 concurrent maintenance capability.

1 U. S. Patent No. 5,822,184 teaches a modular data  
2 device assembly for a computer that has a housing that is  
3 designed to fit into a conventional, industry standard size  
4 expansion bay. Individual plug-in data storage devices,  
5 such as hard disk drive and compact disk drives, are  
6 disposed vertically in a stacked formation within the  
7 housing. A motherboard with plug-in connectors to which the  
8 drives are connected allows easy replacement of defective  
9 data devices, which devices slide in or out. The disk  
10 drives and modular data device assemblies may be arrayed in  
11 either series or in parallel to a controller. By its  
12 modular structure and redundant storage functions, the  
13 device benefits from what is known as the principle of an  
14 redundant array of inexpensive disks.

15 U. S. Patent No. 5,797,667 teaches a hard disk drive  
16 mount. The hard disk drive mount includes a stop bar at  
17 either side of a front end of a hard disk drive frame. The  
18 stop bars prevent a pull handle on a case of the hard disk

1 drive frame from being forcefully pulled up thereby  
2 protecting the hard disk drive lock. The relative movement  
3 between the pull handle and the stop bar results in a  
4 counter force between the case and a mount to cause two  
5 connecting ports to be separated to permit smooth removal of  
6 the hard disk drive.

7 U. S. Patent No. 5,828,548 teaches a removable hard  
8 disk drive mount that includes structures at the end of a  
9 rack. The structures of the disk drive mount cooperate with  
10 structures on the shaft of a pivotal handle to ensure that  
11 final insertion of the hard disk drive casing into the rack  
12 is carried out in an even manner without excessive force.  
13 The mount includes two notches. The two notches are  
14 disposed at the end of a pivotal shaft of a handle for the  
15 casing. Corresponding angles and bent portions of the rack  
16 permit final insertion of the hard disk drive casing into  
17 the rack only when the handle is turned from a horizontal to  
18 a vertical position.

1 U. S. Patent No. 5,886,869 teaches a sleeve that holds  
2 a hard disk drive in portable removable engagement with a  
3 computer. The sleeve includes a base and a cover attachable  
4 to the base by mechanism of threaded fasteners. The base  
5 includes plural holder arms. When the cover is removed from  
6 the base and the hard disk drive advanced into the base, the  
7 holder arms move outwardly as the hard drive rides on the  
8 arms. When the hard disk drive clears the arms, the arms,  
9 which are biased to a hold configuration, move back inwardly  
10 to hold the hard disk drive in the base, with the cover then  
11 attached to the base. The edge of the cover cooperates with  
12 the base to hold the holder arms in the hold configuration  
13 and thereby securely hold the hard disk drive in the sleeve.

14 U. S. Patent No. 5,741,055 teaches a hard disk drive  
15 converting drawer that includes a drawer and a fixing seat.  
16 A pull handle is back and forth slidably disposed at the  
17 front end of bottom side of the drawer. A transverse beam  
18 is disposed between two balance levers of the pull handle

1 for back and forth pulling an auxiliary lever to press and  
2 close/open a touch switch. When the touch switch is pressed  
3 and closed, the auxiliary lever is forced by a leaf spring  
4 disposed on one side to retract the pull handle to a hidden  
5 position. When the touch switch is bounded open, the pull  
6 handle is pushed and ejected for a user to easily pull out  
7 the pull handle. When pulling the pull handle, the  
8 auxiliary lever is driven to push a stopper block projecting  
9 from one side of the fixing seat so as to smoothly draw out  
10 the drawer from the fixing seat for taking out the hard disk  
11 drive.

12 U. S. Patent No. 5,808,871 teaches a modular tower that  
13 has a plurality of bays. The bays are fitted with rails  
14 upon which individual trays are able to slide. The trays  
15 are detailed to carry electronic components. Each tray is  
16 fitted with electrical connectors that are connected to  
17 corresponding electrical connectors on a back plane in the  
18 tower. The tower can be employed vertically and

1 horizontally. The tower has easily removable top and side  
 2 panels. A base may be adjustably secured to one or more of  
 3 the towers. The front thereof is fitted with a removable  
 4 frame that carries a hinged door. The frame can be removed  
 5 and rotated 180 degrees in order to change the swing of the  
 6 door from left to right and vice-versa.

7 U. S. Patent No. 5,788,347 teaches a system that  
 8 interlocks computer peripheral enclosures.

9 U. S. Patent No. 5,067,041 teaches a shielding strip  
 10 for a computer that includes an electrically conductive  
 11 housing and a non-conductive drive mounting structure  
 12 situated within the housing.

13 U. S. Patent No. 5,224,019 teaches a modular computer  
 14 chassis that includes a main chassis. A motherboard is  
 15 attached to the main chassis and a sub-chassis that is  
 16 attachable to the main chassis. The sub-chassis has at  
 17 least one computer component that is attached thereto such  
 18 that the computer component is electrically connected to the

1 motherboard. The computer component is separable from the  
2 main chassis by removing the sub-chassis from the main  
3 chassis. The sub-chassis contains those computer components  
4 subject to mechanical wear and most often requiring  
5 replacement, such as the power supply and hard disk drives.  
6 The sub-chassis is mechanically connected to the main  
7 chassis by using slots and tabs such that when the cover is  
8 in place upon the computer, then the sub-chassis is secured  
9 therein. The sub-chassis and the computer components  
10 disposed thereon thus form a module which is conveniently  
11 removable and replaceable such that those computer  
12 components most frequently requiring service is quickly  
13 removed and replaced by a person unskilled in computer  
14 repair.

15 U. S. Patent No. 5,420,750 teaches computer apparatus  
16 that includes a compartment and a plurality of modules.  
17 Each module has a disk drive memory package and is inserted  
18 in a compartment.

1 U. S. Patent No. 4,754,397 teaches a fault tolerant  
2 computing facility that includes a housing array for  
3 containing a plurality of hardware element modules such as  
4 disk drives, a plurality of modularized power supplies and  
5 plural power distribution modules.

6 U. S. Patent No. 5,227,954 teaches a mounting  
7 arrangement which allows drives of different sizes to be  
8 mounted in a drive dock and the hardware device necessary to  
9 mount full height, half height or third height drives in a  
10 conventional size drive dock. The hardware device provides  
11 mounting plates which have upper and lower ridges for  
12 mounting a single disk drive of varying size within the  
13 single drive dock.

14 U. S. Patent No. 5,222,897 teaches a circuit board  
15 inserter/ejector system which is for inserting a circuit  
16 board into a back plane in a chassis and for ejecting the  
17 circuit board from the back plane of the chassis. The  
18 inserter/ejector system can be used with a magnetic disk



1 drive to facilitate insertion and removal thereof within a  
2 computer.

3 U. S. Patent No. 5,224,020 teaches a modular electrical  
4 apparatus that includes a plurality of customer removable  
5 electrical devices such as disk drives. The devices and  
6 support units are all blind pluggable into a removable  
7 central electrical distribution unit.

8 U. S. Patent No. 5,006,959 teaches a computer apparatus  
9 with modular components including segregated functional  
10 units like a disk array, various plug-in card packages, a  
11 plurality of power supplies, a plurality of fan units and a  
12 motherboard. Another goal for moving towards modular  
13 computer components is to improve reliability. A number of  
14 disk drives are interconnected in an array for redundant  
15 storage of data. Failure of one disk drive does not destroy  
16 irreplaceable data.

17 U. S. Patent No. 5,119,497 teaches a computer that has  
18 modular components and is used/replaced on a common base

1 mount. The computer also has a motherboard with some number  
2 N of connect-receptacles thereon. A deck is superposed to  
3 form a plenum above the motherboard and is characterized by  
4 N like aperture sets, a connector-slot in registry with each  
5 connect-receptacle and several like removable, circuit-  
6 modules. An air cool arrangement is arranged to draw  
7 cooling-air into the circuit-modules and down upon and  
8 across the motherboard.

9 U. S. Patent No. 5,844,776 teaches a static memory  
10 device that has compatibility with a disk drive installed in  
11 an electronic apparatus as an external storage unit. The  
12 static memory device has a first memory board arranged on  
13 the same base portion as the one on which the disk drive can  
14 be installed, the first memory board having the same  
15 projected area as the disk drive.

16 U. S. Patent No. 5,852,546 teaches a computer includes  
17 a chassis and a hard disk drive. The hard disk drive is  
18 mounted in the chassis of the computer.

1 U. S. Patent No. 5,602,696 teaches a computer system  
2 that provides for quick removal and quick and simple  
3 installation of a disk drive into the system.

4 U. S. Patent No. 5,327,308, U. S. Patent No. 5,122,914,  
5 U. S. Patent No. 5,515,215, U. S. Patent No. 5,517,373 and  
6 U. S. Patent No. 5,563,748 teach a computer system that  
7 provides for quick removal and quick and simple installation  
8 of a disk drive unit into the system.

9 U. S. Patent No. 5,119,270 teaches a multimedia data  
10 storage system that includes a compartmentalized drawer  
11 which can be tailored to accommodate the differing cooling  
12 requirements of different device types thus allowing each  
13 device type to be mounted in any device position.

14 U. S. Patent No. 5,207,613 teaches a high-density  
15 electronic module packaging system that includes a cabinet  
16 for housing a plurality of modules. Disposed at the rear of  
17 the cabinet and forming a rear wall thereof is a cooling  
18 system that is used for cooling the modules that are

1 contained in the cabinet. Disposed within the cabinet are  
2 four cooling modules, a power distribution unit having  
3 twelve power converters and twenty-eight electronic modules.  
4 The number of cooling modules, power converters and  
5 electronic modules may be added or subtracted as needed or  
6 desired. The cooling modules allow the flow of cooling  
7 fluid to and/or from the power distribution unit and/or to  
8 the plurality of electronic modules. The power distribution  
9 unit supplies power to the plurality of electronic modules.  
10 The electronic modules may house one or more sub-modules  
11 such as storage disk drives or printed circuit boards.

12 U. S. Patent No. 5,379,184 teaches a multi-module  
13 storage array that includes cabinets. Each cabinet has a  
14 number of identical bays.

15 U. S. Reissue Patent No. 34,369 teaches an adapter that  
16 is provided for a removable information storage device in an  
17 information storage and retrieval system or control system.

18 U. S. Patent No. 5,471,099 teaches a modular enclosure

1 apparatus for a personal computer and a workstation that has  
 2 a plurality of removable and replaceable modules. These  
 3 modules include a cooling module for cooling the enclosure.  
 4 A memory storage device module has a plurality of mating  
 5 slides and carriers for routine installation and removal of  
 6 each memory storage device and a power supply module for  
 7 providing power to the enclosure. The modular enclosure  
 8 apparatus includes a RAID controller module that is  
 9 similarly removable and replaceable. Each module may  
 10 include a circuit interrupt assembly for disabling  
 11 individual modules by cutting off power to the desired  
 12 module and allowing that module or parts contained therein  
 13 to be removed while the power is disabled only to the module  
 14 desired and the remaining modules continue to be enabled.

15 U. S. Patent No. 5,247,427 teaches a disk array system  
 16 that is for use in a data processing system. The disk array  
 17 system includes a chassis having a top wall, a bottom wall,  
 18 two side-walls, an open front end and an open rear end.

1   Disposed within the chassis are three power supplies, a pair  
2   of controller boards, a back plane and a set of twenty disk  
3   drive modules.

4           U. S. Patent No. 5,604,662 teaches an expandable  
5   modular data storage system that includes a plurality of  
6   data storage devices and a plurality of identical,  
7   vertically stackable storage device housings. The housings  
8   slidably receive a data storage device and mechanical  
9   connection elements for releasably connecting the top of one  
10   storage device housing to the bottom of a storage-device  
11   housing stacked thereon.

12          U. S. Patent No. 6,061,250 teaches a full enclosure  
13   chassis system with tool-free access to hot-pluggable  
14   circuit boards therein. The system includes a front access  
15   plate that can be opened and closed from the front of a  
16   primary chassis when mounted within a secondary chassis so  
17   that only the front access panel is externally accessible.  
18   When the front access plate is in an open position, circuit

1 boards within the full enclosure chassis system can be hot-  
2 pluggably removed and inserted without powering down the  
3 system or disrupting any component of the system. The full  
4 enclosure chassis system supplies standard connector access  
5 to the components that are external to the system and are  
6 connected by standard connectors disposed at the back plane  
7 at the back panel of the system.

8 U. S. Patent No. 5,777,845 teaches a disk array which  
9 includes a mainframe chassis conforming to standard SCSI  
10 disk drive mechanical form factors, and a motherboard and a  
11 RAID controller within the chassis. The motherboard  
12 interconnects electrical signals between the electronic  
13 modules, at least some of which include disk drives forming  
14 a redundant array of independent, or inexpensive, disks.  
15 One of the electronic modules is a secondary power supply  
16 module that is connectable to the motherboard. The RAID  
17 controller is electrically connected to a system host power  
18 supply and a host interface bus. Through the RAID

1 controller the motherboard interconnects all the electrical  
 2 signals between the system host power supply and the host  
 3 interface bus with the secondary power supply module and the  
 4 RAID disk drives.

5 U. S. Patent No. 6,065,096 teaches a RAID controller  
 6 which is integrated into a single chip. The RAID controller  
 7 chip includes a general purpose RISC processor, memory  
 8 interface logic, a host central processing unit peripheral  
 9 component-interconnect bus, at least one back-end  
 10 input/output interface channel, at least one direct memory  
 11 access channel, and a RAID parity assist circuit. The RAID  
 12 chip enables higher integration of RAID functions within a  
 13 printed circuit board and in particular enables RAID  
 14 function integration directly on a personal computer or  
 15 workstation motherboard. The back-end input/output  
 16 interface channel is a dual SCSI channel. The RAID chip is  
 17 operable in either of two modes. In a first mode, the chip  
 18 provides pass through from the host central processing unit



1 interface directly to the dual SCSI channels. This first  
 2 mode of operation, a SCSI pass-through mode, allows use of  
 3 the chip for non-RAID storage applications and enables low  
 4 level manipulation of the disk array in RAID applications of  
 5 the chip. The first mode of operation permits use of the  
 6 chip without change to host applications and drivers.

7 Rather, the chip is operable in a manner compatible with  
 8 known available SCSI controller devices. The second mode of  
 9 operation, a RAID control mode, provides full RAID  
 10 management features to the attached host central processing  
 11 unit. The RAID chip presents an intelligent input/output  
 12 interface to the host central processing unit to enhance  
 13 portability and performance of the host/RAID interaction.

14 U. S. Patent No. 6,061,752 teaches a technique that  
 15 allows hot plugging a peripheral controller card, containing  
 16 both a local bus and a peripheral bus on a single connector,  
 17 into a host system board containing a host system bus and a  
 18 host input/output bus. When mating the peripheral

1 controller card to the host system board a local device  
2 power supply is inactive, a peripheral device power bus is  
3 powered, and signal lines of a peripheral device are  
4 maintained in a high impedance state. Following a delay  
5 after the mating, the LDPS is activated by the host  
6 operating system. Following the activation of the LDPS, the  
7 host system bus is coupled to the single connector through  
8 switches that are under OS control. In response to the  
9 activation of the LDPS, the signal lines of the peripheral  
10 device are enabled. The peripheral controller card is a  
11 disk array controller card, the local bus is a peripheral  
12 component-interconnect bus and the peripheral bus is a SCSI  
13 bus. The disk array controller card is coupled to a mass  
14 storage peripheral and is programmed for RAID. A peripheral  
15 component-interconnect bus and a SCSI bus are carried on a  
16 single peripheral connector in order to provide cable  
17 management and readily allow hot plugging a redundant  
18 peripheral controller card into the host system board.

1           U. S. Patent No. 6,058,054 teaches a system that  
2 provides a backup of a portion of a source drive of a  
3 plurality of drives in a redundant array of inexpensive  
4 disks data storage system. The portion of the source  
5 includes a plurality of segments. The system includes a  
6 source drive that is associated with a target drive of the  
7 plurality of drives.

8           U. S. Patent No. 6,018,456 teaches an enclosure system  
9 that receives a number of plug in computer peripheral  
10 devices that includes hard disk drives in a disk array and  
11 utilizes front and rear cages or enclosures that are  
12 separated by a vertical back-plane. The back-plane has  
13 internal circuit interconnections and multi-pin docking  
14 connectors on each face. The system enclosure for receiving  
15 a number of computer peripheral devices to be plugged into  
16 and removed from interconnecting circuits includes front and  
17 rear cage enclosures. Each cage has a rectangular outline  
18 and open front and rear sides relative to an insertion axis

1 the front cage enclosure including guide mechanisms that may  
 2 be positioned at different elevations on the sides thereof  
 3 to accommodate combinations of peripheral devices of  
 4 different heights. A back plane circuit board has front and  
 5 rear faces and being mounted vertically between the rear of  
 6 the front enclosure and the front of the rear enclosure.  
 7 The back plane circuit board includes multi-pin docking  
 8 circuit connectors on the front and rear faces. Each of a  
 9 plurality of computer peripheral holding units may be  
 10 inserted through the front of the front cage enclosure into  
 11 engagement with connectors on the front face of the back  
 12 plane. Each of a plurality of support modules may be  
 13 inserted through the rear opening of the rear enclosure into  
 14 engagement with the connectors on the rear face of the back  
 15 plane.

16 With the advent of disk arrays a number of hard disk  
 17 drives are used together with a data processing system in  
 18 order to provide high storage capacity. Manufacturers early

1 adopted the approach of mounting a number of disk drives in  
2 a single enclosure. This became possible after hard disk  
3 drives and other peripherals became available that were  
4 compact, in successively smaller formats (8 inch, then 5 1/4  
5 inch, then 3 1/2 inch) while achieving high capacity storage  
6 now in the gigabyte range. Host computers and disk drive  
7 controller cards were designed to operate different disk  
8 drives in interrelated fashion on data to be stored by  
9 employing disk striping to distribute a data block among a  
10 number of disk drives. Data distribution was used to  
11 substantially increase data transfer rates, and also to  
12 facilitate error correction, using redundancy to improve  
13 reliability. These developments also led to the  
14 introduction of a number of variants that became referred to  
15 as RAID technology, which now encompasses tape drives as  
16 well as hard disk drives. Among the features adopted were  
17 the use of exchanges of spare drives for defective drives,  
18 and the mounting of disk drives and other peripherals in

1 plug-in cassettes, canisters or trays of different  
2 configurations. These allowed the peripherals to be  
3 inserted into and retained in the enclosure and enabled  
4 completion of electrical power and data transfer  
5 connections. The peripheral devices could be removed in a  
6 "hot swapping" approach that precluded the need for  
7 shutdown. There are now many different types of enclosures  
8 for removable computer peripheral devices that include hard  
9 disk drives in RAID and non-RAID arrays. A number of  
10 adapter systems are available that are employable with a  
11 standard computer port or receptacle (now typically 5 1/4  
12 inches or 3 1/2 inches) to enable a smaller sized peripheral  
13 within a cassette or canister to be removably inserted.  
14 These systems include local controller cards, personality  
15 cards, power supplies and interconnects for the different  
16 computer peripheral devices. As the array systems and  
17 structures have become available in larger volume, system  
18 requirements have come into conflict with demands for

1 greater versatility in use along with customer insistence on  
2 reduced cost. Current demands are for enclosures that can  
3 incorporate different sizes of disk drives, so as to  
4 accommodate different storage capacities or different device  
5 preferences. In 3 1/2 inch drives higher capacity units are  
6 taller (1 5/8 inches) than lower capacity (1 inch high)  
7 units even though both fit within a port or bay designed for  
8 a 3 1/2 inch drive. An enclosure accommodates an integral  
9 number of whatever height drive is to be used or even allow  
10 a mix to be employed. Another respect in which the  
11 enclosure is adaptable relates to the different types of bus  
12 interconnections that may have to be made. The host  
13 input/output connection can be any of a number of different  
14 types. The local bus peripheral bus interconnections can  
15 also be of one of many different types, such as SCSI, IDE,  
16 and peripheral computer interconnect devices. There is also  
17 growing adoption of a connection approach known as Single  
18 Connector Attachment. It is therefore desirable to have

1 available an arrangement in which only a back plane need be  
2 interchanged if different buses are to be used. The  
3 multiple pin mating connectors and cables used with  
4 different buses also should be readily changeable for  
5 different configurations. When configurations change, the  
6 user interface (display and controls) should likewise be  
7 changed to be compatible.

8 U. S. Patent No. 6,331,933 teaches a power sub-frame  
9 that has four walls defining first, second, third and fourth  
10 sides of a open, rectangular frame. A fifth side is open  
11 for receiving a plurality of power supply units for powering  
12 the system unit. A power distribution board located at a  
13 sixth side of the frame opposite to the open side, for  
14 making electrical contact to the power supply units. The  
15 power sub-frame can be manufactured as a sub-assembly for a  
16 system unit, facilitating manufacture, testing and  
17 maintenance. The power sub-frame can be pre-assembled with  
18 power distribution logic and cabling ready to be mounted in



1 a system unit. The system unit can, for example, be a  
2 computer system unit for rack mounting in a  
3 telecommunications application.

4 U. S. Patent No. 6,201,692 teaches a disk drive  
5 enclosure that houses a mix of "slim" and "half high" disk  
6 drive sizes in almost any order. The disk drive enclosure  
7 includes at least thirteen equally spaced pairs of guide  
8 rails.

9 U. S. Patent No. 6,313,983 teaches a computer enclosure  
10 that includes and an inner casing and an outer casing that  
11 receive the inner casing therein. The inner casing includes  
12 a bottom panel and a front panel that extends upright from  
13 the bottom panel. A disk drive rack is arranged above the  
14 bottom panel and attached to the front panel. A main board  
15 support panel supporting a main board is attached to the  
16 front panel, the bottom and the disk drive rack on a first  
17 side of the inner casing. A support member is connected  
18 between the disk drive rack and the bottom panel on an

1 opposite second side of the inner casing for enhancing  
2 mechanical stability of the structure of the inner casing.  
3 The support member has an upper end pivotally attached to  
4 the disk drive rack. The support member is movable between  
5 an open position where the support member is substantially  
6 located outside the inner casing for facilitating  
7 maintenance and a closed position where an lower end of the  
8 support member engages with the bottom panel to releasably  
9 secure the support member to the bottom panel. The support  
10 member defines three sets of openings for selectively and  
11 interchangeably engaging with pawls of three receptacles  
12 containing power supplies of different specifications.  
13 Power supplies of different specifications may be  
14 selectively mounted to the computer enclosure.

15 U. S. Patent No. 6,272,573 teaches a storage system for  
16 a computer system in which the storage capacity can be  
17 incrementally increased without disrupting the operations of  
18 the storage system. The storage system includes a base unit

1 and a plurality of modular units. The modular units are  
2 inserted into the base unit when increased storage capacity  
3 is required. Each modular unit has an enclosure including a  
4 top wall, a bottom wall and two side walls. The bottom and  
5 top walls each have at least one power connector and data  
6 transmission connector. The bottom wall of a first modular  
7 unit attaches to the base unit and the top wall of the first  
8 modular unit can attach to the bottom wall of a second  
9 modular unit. At least one back plane is provided in each  
10 modular unit for providing attachment for a set of storage  
11 devices. A data transmission interconnect system transmits  
12 data and commands between the plurality of storage devices  
13 in the modular units and an external source. In the event  
14 of a loss of connection to any one set of storage devices  
15 data and commands can still be transmitted from and to any  
16 other set of storage devices. Application programs running  
17 on a host system issue requests to access data stored on the  
18 storage devices that are routed through an input/output bus

1 adapter. An input/output bus itself is the medium in which  
2 host commands, disk responses and data are moved between  
3 adapters and the storage devices. A disk controller  
4 connects a host computer's input/output bus to the storage  
5 device input/output bus through a bus adapter channel. The  
6 input/output buses are small computer storage interconnect  
7 devices to the serial storage architecture and the fiber  
8 channel arbitrated loop. Storage subsystems also include  
9 power converters. Power from commercial AC power sources  
10 needs to be converted to DC power and further converted to  
11 supply the amount of power needed for the hard disk drives  
12 and the cooling fans in the subsystem. Redundancy is  
13 required in all aspects of the storage system, including the  
14 hard disk drives, the cooling fans and the power converter  
15 systems.

16 U. S. Patent No. 6,118,776 teaches an apparatus that  
17 provides a fiber channel interconnection between a plurality  
18 of private loop devices through a Fiber Channel private loop

1 device interconnect system. The Fiber Channel private loop  
2 device is connected to two or more Arbitrated Loops  
3 containing one or more private loop devices. The  
4 interconnect system includes a routing filter to filter  
5 incoming arbitrated loop physical addresses to determine  
6 which fiber channel frames must attempt to be routed through  
7 the fabric. Numerous topologies of interconnect systems may  
8 be achieved.

9 U. S. Patent No. 6,338,110 teaches a data storage  
10 system that includes a first storage channel, a first  
11 controller that is coupled to the first storage channel and  
12 a first storage device that is coupled to the first storage  
13 channel. The system also includes a second storage channel,  
14 a second storage device that is coupled to the second  
15 storage channel and a switch that is coupled to the first  
16 storage channel and the second storage channel. The switch  
17 separates the first storage channel from the second storage  
18 channel in a first state and connects the first storage

1 channel and the second storage channel in a second state.  
2 For disk drives and RAM disks, a storage array is commonly  
3 called a disk array, in which a disk controller connects a  
4 host computer to multiple disk drives. The disk controller  
5 provides access to the actual drives in a just a bunch of  
6 drives configuration or performs striping of data across the  
7 drives in a redundant array of independent disks  
8 configuration. Storage channels include an AT Attachment, a  
9 small computer system interface, a fiber channel or storage  
10 system architecture. The external access interfaces often  
11 include industry standard architecture, bus or peripheral  
12 component-interconnect bus for host adapters, SCSI, fiber  
13 channel, or SSA. For tape drives, the storage array  
14 commonly includes individual tapes or tape silos. The  
15 controller may provide data striping capability across the  
16 tapes. The storage channels and external access interfaces  
17 are usually the same as for disk drives. For memory chip  
18 storage devices, the storage array commonly is the main

1 processor memory, cache memory, or other memory subsystem.  
2 The controller commonly performs error detection and  
3 correction (parity and ECC) and provides data striping  
4 (usually called interleaving). The storage channels are the  
5 memory buses. The external access interfaces are commonly  
6 peripheral component-interconnect bus or processor bus. In  
7 order to maintain access to the storage devices in the event  
8 of a single controller failure (to provide high-  
9 availability), two controller cards may be attached to the  
10 same storage devices, in a 'dual-controller' configuration.  
11 One controller may provide access to one set of storage  
12 devices and the other controller may provide access to  
13 another set of storage devices.

14 U. S. Patent No. 6,304,942 teaches a system that  
15 upgrades an original data storage system into an enhanced  
16 data storage system. The data storage system includes a  
17 storage array controller device and a storage array. The  
18 data storage system is initially connected to a host system

1 via a host bus and presents to the host system an original  
2 logical volume mapping associated with a logical identifier  
3 and mapped to a physical data set stored in individual  
4 storage devices in the storage array. An enhanced storage  
5 array controller device is operatively connected to the host  
6 system via the host bus and the storage array controller  
7 device and a storage connection device coupled to another  
8 storage array are operatively connecting to the enhanced  
9 storage array controller device via an intermediate bus.  
10 The physical data set of the storage array is redistributed  
11 across a plurality of storage arrays, including the original  
12 and the other storage arrays. The enhanced storage array  
13 controller device is reconfigured to present to the host  
14 system a logical volume mapping associated with the logical  
15 identifier mapping to the physical data set redistributed  
16 across the plurality of storage arrays. Other storage  
17 controller cards do not provide RAID functionality and  
18 merely present the host system with access to multiple disks



1 in an enclosed array (often referred to as a JBOD) without  
2 providing the virtual disk functionality. When a JBOD is  
3 connected via a SCSI bus, for example, each disk is  
4 addressed with both a SCSI Target ID and at least one LUN.  
5 Such storage controller cards do not provide RAID  
6 functionality, but are intended primarily to provide access  
7 to multiple disks and to monitor the environment within a  
8 disk array enclosure.

9 U. S. Patent No. 6,243,787 teaches an apparatus that  
10 conveys data over a packet-switching network. Data are  
11 received from a peripheral device for transmission via the  
12 network to a memory associated with a central processing  
13 unit, followed by an interrupt signal from the peripheral  
14 device associated with the data. One or more data packets  
15 containing the data are sent over the network to a host  
16 network interface serving the memory and the central  
17 processing unit, followed by an interrupt packet sent over  
18 the network to the host network interface. Responsive to

1 the interrupt packet, an interrupt input of the central  
2 processing unit is asserted only after the one or more data  
3 packets have arrived at the host network interface. In  
4 current-generation computers, the central processing unit is  
5 connected to the system memory and to peripheral devices by  
6 a parallel bus, such as the ubiquitous Peripheral Component  
7 Interface bus. As data path-widths grow, and clock speeds  
8 become faster, however, the parallel bus is becoming too  
9 costly and complex to keep up with system demands. In  
10 response, the computer industry is moving toward fast,  
11 packetized, serial input/output bus architectures, in which  
12 computing hosts and peripheral are linked by a switching  
13 network, commonly referred to as a switching fabric. A  
14 number of architectures of this type have been proposed,  
15 including "Next Generation input/output" and "Future  
16 input/output," culminating in the "INFINIBAND" architecture,  
17 which has been advanced by a consortium led by a group of  
18 industry leaders (including Intel, Sun, Hewlett Packard,

1 IBM, Compaq, Dell and Microsoft). Storage Area Networks  
 2 provide a similar, packetized, serial approach to high-speed  
 3 storage access, which can also be implemented using an  
 4 INFINIBAND fabric. In a parallel bus-based computer system,  
 5 when a peripheral device needs to deliver data to the  
 6 central processing unit, it typically writes the data to the  
 7 memory over the bus, using direct memory access. When the  
 8 peripheral has finished writing, it asserts an interrupt to  
 9 the central processing unit on one of the interrupt lines of  
 10 the bus. Bus arbitration ensures that the central  
 11 processing unit will not attempt to read the data from the  
 12 memory until the writing of the data is complete. When a  
 13 packet-switching fabric connects a peripheral device and a  
 14 central processing unit they operate asynchronously. The  
 15 data sent to the memory and the interrupt to the central  
 16 processing unit travel over different paths, or channels. A  
 17 separate line or channel is provided to connect the  
 18 interrupt pin of the peripheral device to an interrupt

1 controller of the central processing unit, bypassing the  
2 switching fabric. There is no a priori assurance that all  
3 of the data will have been written to the memory before the  
4 central processing unit begins reading.

5 The inventor hereby incorporates all of the above  
6 referenced patents into this specification.

7 SUMMARY OF INVENTION

8 The present invention is generally directed to a casing  
9 of a peripheral enclosure and a plurality of canisters. The  
10 peripheral enclosure includes a power supply, a back plane,  
11 a personality card and a cooling unit. The peripheral  
12 enclosure may also include controller card.

13 In a first separate aspect of the invention the  
14 computer peripheral enclosure includes at least one  
15 additional power supply that is removable.

16 In a second separate aspect of the invention the  
17 computer peripheral enclosure includes at least one  
18 additional cooling unit that is removable.

1        In a third separate aspect of the invention the  
2 computer peripheral enclosure includes at least one  
3 additional controller card that is removable.

4        In a fourth separate aspect of the invention the  
5 computer peripheral enclosure includes at least one  
6 additional personality card that is removable.

7        In a fifth separate aspect of the invention the back  
8 plane of the casing has holes in a pattern that direct a  
9 balanced airflow all around the casing.

10       In a sixth separate aspect of the invention the  
11 controller cards are networked by a side riser card and four  
12 peripheral component-interconnect cards. The peripheral  
13 component-interconnect card includes computer cards, network  
14 cards, controller cards, personality cards and memory cards  
15 in any permutation and combination thereof.

16       Other aspects and many of the attendant advantages will  
17 be more readily appreciated as the same becomes better  
18 understood by reference to the following detailed

1 description. The features of the present invention which  
2 are believed to be novel are set forth with particularity in  
3 the appended claims.

4 DESCRIPTION OF DRAWINGS

5 Fig. 1 is a perspective drawing of a canister that has  
6 a combined guide rail and light pipe system and a cam  
7 mechanism.

8 Fig. 2 is a front plan view of the canister of Fig. 1.

9 Fig. 3 is a rear plan view of the canister of Fig. 1.

10 Fig. 4 is a top plan view of the canister of Fig. 1.

11 Fig. 5 is a bottom plan view of the canister of Fig. 1.

12 Fig. 6 is a schematic drawing of the canister of Fig. 1  
13 as the canister is being inserted into one of the  
14 compartments of a casing.

15 Fig. 7 is a perspective drawing of a plastic light  
16 carrier of the combined guide rail and light pipe system of  
17 the canister of Fig. 1.

18 Fig. 8 is a perspective drawing of a guide rail of the

1 combined guide rail and light pipe system of the canister of  
2 Fig. 1.

3 Fig. 9 is a schematic drawing of the light pipe system  
4 of the canister of Fig. 1

5 Fig. 10 is a partial schematic drawing of the light  
6 pipe system of Fig. 9.

7 Fig. 11 is a perspective drawing of a canister that has  
8 a combined lock-cam mechanism and light pipe system.

9 Fig. 12 is a front plan view of the canister of Fig.  
10 11.

11 Fig. 13 is a rear plan view of the canister of Fig. 11.

12 Fig. 14 is a partial top plan view of the canister of  
13 Fig. 11.

14 Fig. 15 is a partial bottom plan view of the canister  
15 of Fig. 11.

16 Fig. 16 is a perspective drawing of a storage device of  
17 U. S. Patent No. 5,822,184 that is a hard disk drive.

18 Fig. 17 is a perspective drawing of a two storage-

1 device peripheral enclosure with three canisters that have  
2 two storage devices and one front removable power supply.

3 Fig. 18 is a rear elevation view of the two storage-  
4 device peripheral enclosure of Fig. 17.

5 Fig. 19 is a perspective drawing of a six storage-  
6 device peripheral enclosure with eight canisters which have  
7 six storage devices and two front-removable power supplies.

8 Fig. 20 is a rear elevation view of the six storage-  
9 device peripheral enclosure of Fig. 19.

10 Fig. 21 is a perspective drawing of a four storage-  
11 device peripheral enclosure with five canisters which has  
12 four storage devices and one front-removable power supply.

13 Fig. 22 is a rear elevation view of the four storage-  
14 device peripheral enclosure of Fig. 19.

15 Fig. 23 is a perspective drawing of an eight storage-  
16 device peripheral enclosure with ten canisters that have  
17 eight storage devices and two front-removable power  
18 supplies.



1        Fig. 24 is a rear elevation view of the eight storage-  
2 device peripheral enclosure of Fig. 23.

3        Fig. 25 is a perspective drawing of a ten storage-  
4 device peripheral enclosure with ten canisters that have ten  
5 storage-devices.

6        Fig. 26 is a rear elevation view of the ten storage-  
7 device peripheral enclosure of Fig. 25.

8        Fig. 27 is a perspective drawing of a twenty storage-  
9 device peripheral enclosure with twenty canisters that have  
10 twenty storage devices.

11       Fig. 28 is a front perspective drawing of a rack-  
12 mountable, ten storage-device peripheral enclosure.

13       Fig. 29 is a rear elevation view of the rack-mountable,  
14 ten storage-device peripheral enclosure of Fig. 28.

15       Fig. 30 is a front exploded view of the rack-mountable,  
16 storage-device peripheral enclosure of Fig. 28.

17       Fig. 31 is a front exploded view of the rack-mountable,  
18 ten storage-device peripheral enclosure of Fig. 28.

1        Fig. 32 is a first rear exploded view of the rack-  
2 mountable, ten storage-device peripheral enclosure of Fig.  
3 28.

4        Fig. 33 is a second rear exploded view of the rack-  
5 mountable, ten storage-device peripheral enclosure of Fig.  
6 28.

7        Fig. 34 is a rear exploded view of the rack-mountable,  
8 ten storage-device peripheral enclosure of Fig. 28.

9        Fig. 35 is a partial perspective drawing of a rail  
10 assembly for a rack-mountable, ten storage-device peripheral  
11 enclosure.

12       Fig. 36 is a partial outside, elevation side view of  
13 the rail assembly of Fig. 35 for a rack-mountable ten  
14 storage-device peripheral enclosure.

15       Fig. 37 is a partial inside, elevation side view of the  
16 rail assembly of Fig. 35 for a rack-mountable ten storage-  
17 device peripheral enclosure.

18       Fig. 38 is a partial outside, front perspective view of

1 the rail assembly of Fig. 35 for a rack-mountable ten  
2 storage-device peripheral enclosure.

3 Fig. 39 is a front perspective drawing of one of the  
4 screws for the rail assembly of Fig. 35.

5 Fig. 40 is a front perspective drawing of the rack-  
6 mountable, ten storage device peripheral enclosure of Fig.  
7 28 showing the path of air which is being drawn into and  
8 exhausted from the rack-mountable, ten storage device  
9 peripheral enclosure.

10 Fig. 41 is a side schematic drawing of the rack-  
11 mountable, ten storage device peripheral enclosure of Fig.  
12 28 also showing the path of air which is being drawn into  
13 and exhausted from the rack-mountable, ten storage device  
14 peripheral enclosure.

15 Fig. 42 is a rear perspective drawing of a ten storage-  
16 device peripheral enclosure that includes a back plane, two  
17 RAID controller cards, a personality board and a SAF-TE/SES  
18 board.

1        Fig. 43 is a front perspective drawing of the ten  
2 storage device peripheral enclosure of Fig. 42.

3        Fig. 44 is a schematic drawing of SCSI software and an  
4 electrical diagram of a peripheral enclosure.

5        Fig. 45 is a front perspective drawing of a first rack-  
6 mountable fifteen storage-device peripheral enclosure that  
7 includes a fifteen drive back-plane, two controller cards  
8 and two personality boards according to the first  
9 embodiment.

10       Fig. 46 is a rear elevation view of the first rack-  
11 mountable, fifteen storage-device peripheral enclosure of  
12 Fig. 45.

13       Fig. 47 is a front exploded view of the first rack-  
14 mountable, fifteen storage-device peripheral enclosure of  
15 Fig. 45.

16       Fig. 48 is a rear perspective view of the first rack-  
17 mountable fifteen storage-device peripheral enclosure of  
18 Fig. 45.

1        Fig. 49 is a front perspective view of the first rack-  
2 mountable fifteen storage-device peripheral enclosure of  
3 Fig. 45.

4        Fig. 50 is a rear elevation view of a second rack-  
5 mountable, fifteen storage-device peripheral enclosure that  
6 includes dual personality boards according to the second  
7 embodiment.

8        Fig. 51 is a front perspective drawing of a third rack-  
9 mountable fifteen storage device peripheral enclosure that  
10 includes a fifteen drive back plane and dual personality  
11 boards according to the third embodiment.

12       Fig. 52 is a rear elevation view of the third rack-  
13 mountable, fifteen storage-device peripheral enclosure of  
14 Fig. 51 that includes dual personality cards.

15       Fig. 53 is a rear perspective view of the third rack-  
16 mountable fifteen storage-device peripheral enclosure of  
17 Fig. 51.

18       Fig. 54 is a rear exploded view of the third rack-

1 mountable fifteen storage-device peripheral enclosure of  
2 Fig. 51.

3 Fig. 55 is an exploded view of dual personality boards  
4 and a container for the dual personality boards of the third  
5 rack-mountable fifteen storage-device peripheral enclosure  
6 of Fig. 51.

7 Fig. 56 is a front view of the container of the third  
8 rack-mountable fifteen storage-device peripheral enclosure  
9 of Fig. 55.

10 Fig. 57 is a rear perspective view of a container that  
11 a user may insert his own controller cards and personality  
12 boards, a back plane and a casing for a fourth rack-  
13 mountable fifteen storage-device peripheral enclosure  
14 according to the fourth embodiment.

15 Fig. 58 is a rear perspective view of the container and  
16 the back plane for the fourth rack-mountable fifteen  
17 storage-device peripheral enclosure of Fig. 57.

18 Fig. 59 is a rear perspective view of a fifth rack-

1 mountable fifteen storage-device peripheral enclosure that  
 2 includes a fifteen drive back plane, dual personality boards  
 3 and two carriers for the dual personality boards according  
 4 to the fifth embodiment.

5 Fig. 60 is an exploded rear perspective view of the  
 6 dual personality boards, the two carriers and the back plane  
 7 for the fifth rack-mountable fifteen storage-device  
 8 peripheral enclosure of Fig. 59.

9 Fig. 61 is an exploded rear perspective view of one of  
 10 the dual personality boards and one of the two carriers for  
 11 the fifth rack-mountable fifteen storage-device peripheral  
 12 enclosure of Fig. 59.

13 Fig. 62 is a rear perspective view of a sixth rack-  
 14 mountable fifteen storage-device peripheral enclosure that  
 15 includes a fifteen drive back plane, dual personality boards  
 16 and two carriers for the dual personality boards according  
 17 to the sixth embodiment.

18 Fig. 63 is an exploded rear perspective view of the

1 dual personality boards and the back plane for the sixth  
2 rack-mountable fifteen storage-device peripheral enclosure  
3 of Fig. 63.

4 Fig. 64 is a rear perspective view of casing, a circuit  
5 board, a container that a user may insert his own controller  
6 cards and personality boards, a back plane for a seventh  
7 rack-mountable fifteen storage-device peripheral enclosure  
8 according to the seventh embodiment.

9 Fig. 65 is a rear perspective view of the circuit  
10 board, the container and the back plane for a seventh rack-  
11 mountable fifteen storage-device peripheral enclosure of  
12 Fig. 64.

13 Fig. 66 is a rear perspective view of the circuit board  
14 and the back plane for a seventh rack-mountable fifteen  
15 storage-device peripheral enclosure of Fig. 64.

16 Fig. 67 is a front perspective view of casing and a  
17 back plane for an eighth rack-mountable fifteen storage-  
18 device peripheral enclosure according to the eighth



1 embodiment.

2 Fig. 68 is a front perspective view of the back plane  
3 and handles and light pipes of the fifteen storage devices  
4 for the eighth rack-mountable fifteen storage-device  
5 peripheral enclosure of Fig. 67.

6 Fig. 69 is a front perspective view of the back plane  
7 and the light pipes of the fifteen storage devices for the  
8 eighth rack-mountable fifteen storage-device peripheral  
9 enclosure of Fig. 67.

10 Fig. 70 is a rear perspective view of a canister, an  
11 electrical converter and a storage device according to the  
12 ninth embodiment.

13 DESCRIPTION OF THE PREFERRED EMBODIMENT

14 Referring to Fig. 1 in conjunction with Fig. 2 and Fig.  
15 3 a canister 10 includes a u-shaped tray 11 and two guide-  
16 rails 12. The u-shaped tray 11 has a rectangular cross-  
17 section and a depth. The canister 10 also includes a light  
18 pipe system 13 and a cam mechanism 14. The u-shaped tray 11

1 has two side walls 15 and two return lips 16. The canister  
2 10 includes a front plate 17 and a handle 18. The front  
3 plate 17 is mechanically coupled to the u-shaped tray 11.

4 Referring to Fig. 4 in conjunction with Fig. 1 and Fig.  
5 5 each guide rail 12 is mechanically coupled to one of the  
6 two side walls 15 of the u-shaped tray 11. The cam  
7 mechanism 14 has a pivot mount 19 and a pin 20. The pivot  
8 mount 19 mechanically couples the handle 18 to the u-shaped  
9 tray 11. The two lips 16 are adjacent to the bottom surface  
10 of the u-shaped tray 11 at its rear end. The u-shaped also  
11 has a plurality of flat springs 21 that are disposed on the  
12 top outside surface and the bottom outside surface of the  
13 canister 10. The handle 18 has a plastic insert 22 that is  
14 used for a commercial designation of a customer. The  
15 customer may choose the color and texture of the plastic  
16 insert 22.

17 Referring to Fig. 6 in conjunction with Fig. 1 a  
18 computer peripheral enclosure has a casing 110. The casing

1 110 has a plurality of compartments 111, two guide rail  
2 tracks 112, a slot 113 and a back plane 114. Each  
3 compartment 111 is rectangular and has a cross-sectional  
4 area and a depth. The compartment 111 also has two rail  
5 guide tracks 112. The cross-sectional area and the depth of  
6 the u-shaped tray 11 are slightly less than the cross-  
7 sectional area and the depth of the compartment 111,  
8 respectively. The u-shaped tray 11 of each canister 10 is  
9 able to slide freely, but snugly, into one of the  
10 compartments 111 of the casing 110. The guide rails 12 of  
11 the u-shaped tray 11 slide freely, but snugly, onto the rail  
12 guide tracks 112 of the compartment 111. The two lips 20 of  
13 the u-shaped tray 11 assist the user as he inserts the u-  
14 shaped tray 11 of each canister 10 into one of the  
15 compartments 111 of the casing 110. The flat springs 21  
16 electrically couple each canister 10 to one of the  
17 compartments 111 of the casing 110 in order to either  
18 eliminate or reduce electro-magnetic interference. The

1 handle 17 either inserts or removes the canister 10 from the  
2 casing 110 of the peripheral enclosure with just enough  
3 either insertion force or removal force to ensure that a  
4 storage device is either connected or disconnected and that  
5 its connectors are not damaged.

6 Referring to Fig. 7 in conjunction with Fig. 8 the  
7 light pipe system 13 includes a light pipe 115 and a special  
8 lens 116. The light pipe 115 has an input end and an output  
9 end. The area of the input end is larger than the area of  
10 the output end. The special lens 116 has a first light  
11 diffuser 117 on a first surface and a second light diffuser  
12 118 on a second surface. The first and second light  
13 diffusers 117 and 118 are a frosted first surface and a  
14 frosted second surface of the special lens 116. The special  
15 lens 116 is mechanically coupled to handle 18. The light  
16 pipe 115 has a protrusion 119 which engages an indent 120 in  
17 the side wall 15 of the u-shaped tray 11 in order to  
18 properly align the light pipe 115.

1 Referring to Fig. 9 in conjunction with Fig. 10 the  
2 light pipe 115 and the special lens 116 are optically  
3 coupled to two light emitting diodes 121 and 122. One light  
4 emitting diode 121 emits a red light and the other light  
5 emitting diode 122 emits a blue light. When the red and  
6 blue lights are mixed the resulting mixture of light is a  
7 purple light. The light emitting diodes 121 and 122 are  
8 mounted onto the back plane 114. The light from each set of  
9 two light emitting diodes 119 and 120 travels through the  
10 air and into the light pipe 115 of one of the canisters 10.

11 The light pipe 115 is mounted within one of the guide rails  
12 12 to form a combined guide rail and light pipe system 119.

13 The combined guide rail and light pipe system 119 is  
14 mounted on one of the two side walls 15 of the u-shaped tray  
15 11.

16 Referring to Fig. 11 in conjunction with Fig. 12 and  
17 Fig. 13 the canister 210 includes a u-shaped tray 211, two  
18 guide rails 212, a light pipe system 213 and a cam mechanism

1 214. The u-shaped tray 211 is rectangular and has a cross-  
2 sectional area and a depth. The u-shaped tray 211 has two  
3 side walls 215 and two return lips 216. The canister 210  
4 includes a front plate 217 and a handle 218. The front  
5 plate 217 is mechanically coupled to the u-shaped tray 211.  
6 The cam mechanism 214 has a pivot mount 219 and a pin 220.  
7 The pivot mount 219 mechanically couples the handle 218 to  
8 the u-shaped tray 211. The two lips 216 are adjacent to the  
9 bottom surface of the u-shaped tray 11 at its rear end. The  
10 u-shaped also has a plurality of flat springs 221 that are  
11 disposed on the top outside surface and the bottom outside  
12 surface of the canister 20. The handle 218 has a plastic  
13 insert 222 that is used for a commercial designation of a  
14 customer. The customer may choose the color and texture of  
15 the plastic insert 222.

16 Referring to Fig. 14 in conjunction with Fig. 11 and  
17 Fig. 15 the canister 210 further includes a lock 223. The  
18 lock 223 locks the handle 218 to the front plate 217 so that

1 neither installation into nor removal of the canister 210  
2 from the compartment 111 of the casing 110 of the computer  
3 peripheral enclosure can occur once the lock 223 has been  
4 engaged.

5 Referring to Fig. 16 a storage device, such as a hard  
6 disk drive, can be of any given configuration. The storage  
7 device is readily available in the commercial market and  
8 known in the art. The back of the standard storage device  
9 has drive-ready connectors. If the storage device is not  
10 drive-ready then the storage device can be converted by  
11 either a forty-pin or an eighty-pin high density drive ready  
12 connector to make the storage device RAID ready. Other  
13 types of connectors can be used depending upon design  
14 requirements.

15 Referring to Fig. 17 through Fig. 34 there are  
16 peripheral enclosures that use the technology of this patent  
17 application. There are several different configurations:  
18 two drive bays through twenty drive bays tower peripheral

1 enclosures and ten through sixteen drive bays rack-mountable  
2 peripheral enclosures. These peripheral enclosures are  
3 built to house all types of computer peripheral equipment  
4 including DAT drives, storage devices, optical drives, CD-  
5 ROM drives, DVD drives, any tape drive, any type storage  
6 devices and network attached storage servers along with up  
7 to two RAID controller cards. The RAID controller cards are  
8 fault- tolerant storage management devices. The connections  
9 on the SCSI units are for eighty-pin SCA drives, but there  
10 are boards that work for other interfaces. A fiber channel  
11 unit uses forty-pin connections. Configurations vary  
12 depending on the scalability requirements of the user. The  
13 rack-mountable bay casing can hold either up to ten 3.5" HH  
14 or up to sixteen 3.5" LP storage devices.

15 Referring to Fig. 35 a rail assembly 251 for the rack-  
16 mountable bay peripheral enclosure includes two L-shaped  
17 members.

18 Referring to Fig. 36 in conjunction with Fig. 35 and



1 Fig. 37 the rail assembly 251 also includes two extension  
2 members 252.

3 Referring to Fig. 38 in conjunction with Fig. 35 and  
4 Fig. 39 each L-shaped member of the rail assembly 251 is  
5 attached to a side wall 253 of a cabinet 254 by screws 255.

6 Referring to Fig. 40 in conjunction with Fig. 41 a ten  
7 device peripheral enclosure 310 includes ten storage devices  
8 311, ten canisters 312, two high-speed blowers 313, a back  
9 plane 314, two power supplies 315, two controller cards 316  
10 and two personality cards 317. The blowers 313 are used to  
11 provide a pressurized cooling system. The ten storage-disk  
12 drives 111, the ten canisters 112, the two blowers 113, the  
13 back-plane 114, the power supplies 115 and the controller  
14 cards 116 have special venting locations. The venting  
15 locations provide the entire chassis with a balance of  
16 cooling within the ten device peripheral enclosure 310  
17 across all storage devices 311. The back plane 314 has  
18 holes 320 in a hole-pattern. The design of the hole-pattern

1 directs a balanced airflow all around the chassis of the ten  
 2 device peripheral enclosure 310. In the canister 312 there  
 3 are venting holes 321 in the u-shaped tray 331, the side  
 4 walls 335, the front plate 336 and the handle 337. These  
 5 venting holes 321 channel air over the installed storage  
 6 devices 311 in order to keep them operating within their  
 7 thermal operating specifications under normal room  
 8 temperature. The canister 312 further includes a  
 9 ventilation system 340 that includes a pattern of holes 342  
 10 in the front plate 334, the side walls 335 of the u-shaped  
 11 tray 331 and the handle 337. The pressurized system of the  
 12 ventilation system 340 allows the power supplies 315, the  
 13 blowers 313 and controller cards 316 to be in the back. The  
 14 back plane 314 is in the middle. The storage devices 311  
 15 are in the front in a 3U space while still cooling  
 16 everything better than a 5U unit of similar specifications.  
 17 The entire system relies on the use of this pressurization  
 18 for cooling. This type of pressurization in such a small

1 form factor with so many things to cool in this small amount  
2 of space does not exists in the prior art.

3 Referring to Fig. 42 in conjunction with Fig. 43 two  
4 controller bays are located in the back of the ten device  
5 peripheral enclosure 310 and plug into a riser card 351.  
6 The riser card 351 plugs into a personality card 317. The  
7 personality card 317 plugs into a back plane 314. The  
8 personality card 317 is connected to the two power supplies  
9 315, SAF-TE/SES and to the drives of storage devices 311  
10 that are connected to the opposite side of the back-plane  
11 314.

12 Referring to Fig. 43 in conjunction with Fig. 42 the  
13 advanced technology back plane 314 acts as a connection  
14 point for all of the components that make up the ten  
15 storage-device peripheral enclosure 310. These components  
16 include the storage device drives 311, the blowers 313,  
17 controller cards 316, personality cards 317 and power  
18 supplies 315. Using board-to-board connectivity, there is a

1 bus impedance matching to achieve maximum data transfer  
2 rates and reduce error rates.

3 Referring to Fig. 44 in conjunction with Fig. 42 the  
4 ten storage-device peripheral enclosure 310 includes ten  
5 storage devices 311, ten canisters 312, two blowers, a back  
6 plane 314, two power supplies 315, two controller cards 316  
7 and a personality board 317. The ten storage devices 311  
8 connect directly to the back plane 314 thereby reducing  
9 cabling and improving both functionality and reliability.  
10 The SAF-TE/SES, SCSI termination and other features are all  
11 built onto the back plane 314. There is a split bus in that  
12 the back plane 314 may be divided into dual channels. A  
13 SCSI Accessed Fault Tolerant Enclosure module may be plugged  
14 into a personality board 317 in order to provide the user  
15 with the ability to monitor all of the aspects of the  
16 enclosure. These aspects include cooling, power and device  
17 activity from a remote computer system that is connected to  
18 a ten storage-device peripheral enclosure 310 through a host

1 or network connection. The module is designed to be  
2 universally plugged in it has two connections, one on each  
3 end. The module can be plugged in one way in order to  
4 enable it on the host channel or be flipped around the other  
5 way to enable it on the drive channel. The way the module  
6 is plugged in determines how it will interface with the host  
7 system.

8 Referring to Fig. 44 in conjunction with Fig. 42 and  
9 Fig. 43 a ten storage-device peripheral enclosure 310 is  
10 modular and fault-tolerant. There can be more than one  
11 power supply 315, more than one blower 313 and more than  
12 controller card 316. If any one of the components fails,  
13 the other one will continue to adequately run the ten  
14 storage-device peripheral enclosure 310. If one of the  
15 power supplies 315 fails out there is at least one power  
16 supply 315 still left. The remaining power supply will  
17 provide enough power to run the ten storage-device  
18 peripheral enclosure 310. The modular components are hot

1 swappable so that the unit does not have to be shut down to  
2 replace the component. If a power supply 315 has failed it  
3 is simply replaced with a new power supply 315 while the ten  
4 storage-device peripheral enclosure 310 continues to run  
5 uninterrupted. The special connectors that are used on the  
6 power supplies 315 have specially designed pins that are  
7 staggered and allow hot swapping. If the user decides to  
8 install two controller cards 316, they can be setup in a  
9 fault-tolerant configuration. This configuration provides  
10 that if one of the controller cards 316 fails, the other  
11 controller card 316 will continue to work without causing  
12 system downtime. The special connectors that are used on  
13 the back plane 314 allow the failed controller card 316 to  
14 be replaced while the system continues to function.

15 If the user decides that he wants to be able to monitor  
16 a ten storage-device peripheral enclosure 310 from a remote  
17 location, he can add a SAF-TE/SES to the personality board  
18 317. This is compliant to an industry standard. The design

1 of a ten storage-device peripheral enclosure 310 has  
2 expanded on this industry standard and has added many vendor  
3 unique upgrades to its product. The design of a ten  
4 storage-device peripheral enclosure 310 also implements a  
5 proprietary serial-based interface in order to communicate  
6 with this SAF-TE/SES located on the back plane 314. This is  
7 called either an 'S-link' or an SAF-TE/SES link. It is a  
8 serial communications connection using a connector. Every  
9 peripheral enclosure that is part of the series has an S-  
10 Link connector on it. This allows the user to install a  
11 SAF-TE/SES into one peripheral enclosure and connect it to  
12 another peripheral enclosure using the S-Link to connect the  
13 other peripheral enclosure to the SAF-TE/SES. This way, the  
14 user can monitor all of the peripheral enclosures in his  
15 system using just one SAF-TE/SES. A rack-mountable, ten  
16 storage-device peripheral enclosure 310 fits into an  
17 industry-standard 19" cabinet space in a 3U form factor.  
18 The ten storage-device peripheral enclosure 310 has a

1 special two piece mounting tray which incorporates  
2 simplified integrating hardware. The technology of the back  
3 plane 314 allows a number of storage devices 311, controller  
4 cards 316, redundant power supplies 315, redundant blowers  
5 313 and SAF-TE/SES to be all within this small amount of  
6 space. No one else has been able to engineer a system like  
7 this within this amount of space. A ten storage-device  
8 peripheral enclosure 310 allows all major components to be  
9 removable and hot swappable so that the system is not  
10 compromised if one of these components fails. Without using  
11 the design of the back plane 314 the smallest, similarly  
12 featured enclosure which can be created would be 5U high.  
13 Power supplies 315 and blowers 313 would be in the back of  
14 the unit and the two controller cards 316 and ten storage  
15 devices 311 would be in the front.

16 A large number of storage devices 311 and controller  
17 cards 316 can fit into this 3U space including two  
18 controller cards 316 and ten storage devices 311. In any



1 other enclosures of the prior art, the controller cards 316  
2 and the storage devices 311 would be placed into a separate  
3 chassis. A chassis for a controller card 316 is typically  
4 4U and a chassis for storage disk drive 311 is typically 3U  
5 and usually holds either eight or nine storage devices 311  
6 at the most. The casing 310 holds ten storage devices 311.  
7 The same amount of storage devices 311 or more may fit into  
8 the casing 310 than in a 7U enclosure of the prior art that  
9 requires 3U's for storage devices and 4U's for controller  
10 enclosure. Even the most compact systems of the prior art  
11 that combine controller cards and drives into the same  
12 peripheral enclosure can be no smaller than 5½ inches.  
13 These systems can not come close to the casing 310 in  
14 performance or reduced points of failure because they have  
15 cabling and normally several different boards to make all of  
16 the connections to all devices.

17 The casing of a ten storage-device peripheral enclosure  
18 310 complies with agency standards such as UL, CSA/TUV, C-

1 Tick and CE. A conversion kit allows the rack-mountable  
2 ten-bay peripheral enclosure 310 to be made into either a  
3 ten-bay or a twenty-bay tower by adding on a plastic bottom  
4 and a side shell thereby turning it into a permanent tower  
5 peripheral enclosure. This universality to the peripheral  
6 enclosure 310 allows out the users to choose their migration  
7 pattern. A twenty-bay tower conversion kit requires two  
8 ten-bay rack-mountable peripheral enclosures 310. These  
9 peripheral enclosures 310 are configured in a RAID setup  
10 that is a Redundant Array of Independent Disks. If any one  
11 of the storage devices fails then the remaining device  
12 recalculate the missing data. These peripheral enclosures  
13 310 offer many features that allow data to remain online and  
14 accessible such as hot-swap blowers, hot-swappable fans and  
15 hot-swappable removable canister. These features allow the  
16 highest data availability possible and are meant to keep  
17 data accessible even if a failure does occur. Depending on  
18 the RAID configuration used if an entire subsystem enclosure

1 fails completely, the other remaining subsystem enclosures  
2 will be able to calculate and rebuild the data which was on  
3 the failed subsystem enclosure peripheral enclosure so that  
4 no system data loss will occur.

5 In order to prevent a unit failure while operating, the  
6 power supply pins are designed in such a way that when  
7 either removing or installing the power supply the system is  
8 not disrupted. This is done by connecting a zero voltage  
9 line and an option line first, connecting the interface  
10 lines and the DC power lines. This provides a safe means of  
11 power supply replacement while the unit continues to  
12 operate. The output connector on the power supply has three  
13 different length pins for staggered engagement when hot  
14 swapping. Only two lengths were needed for the power supply  
15 but the connector was selected because of its availability.  
16 Pin 19, DC Enable, must engage last so that the all outputs  
17 are connected before enabling the output. Controller cards  
18 in the casings can be networked by a side riser card at four

1 peripheral component interconnect cards. The peripheral  
2 component-interconnect card is a computer card plus three  
3 cards or a personality board 317 and four peripheral  
4 component-interconnect cards.

5 Referring to Fig. 45 in conjunction with Fig. 46 and  
6 Fig. 47 a first fifteen device peripheral enclosure 410  
7 includes fifteen storage devices 411, fifteen canisters 412,  
8 two blowers 413, a back plane 414, two power supplies 415,  
9 two controller cards 416 and two personality boards 417.  
10 The personality board 417 and controller cards 416 are  
11 located in the back of the fifteen-bay peripheral enclosure  
12 410. The personality boards 417 plug into the back plane  
13 414. The personality boards 417 are connected to the two  
14 power supplies 415. The personality boards 417 are also  
15 connected to the storage devices 411 that are connected to  
16 the opposite side of the back plane 414. The fifteen-bay  
17 peripheral enclosure 410 may include an array of fifteen  
18 Small Computer System Interface storage disk drives 411 that

1 have a low voltage differential. The fifteen-bay peripheral  
 2 enclosure 410 may be used in either Just-A Bunch of Drives  
 3 applications or Redundant Array of Independent Disks  
 4 applications. Option for universal interface include the  
 5 following host connectivity or data bus technology Small  
 6 Computer System Interface, Fiber Channel Arbitrated Loop,  
 7 IEEE 1394 "Firewire", Advanced Technology Attachment Packet  
 8 Interface, Serial ATA, Ethernet, such as 10/100Tx and GB  
 9 Ethernet, for Network Attached Storage, Internet Small  
 10 Computer System Interface and InfiniBand. In the space of  
 11 7" x 5.2" on the rear panel either removable Just A Bunch of  
 12 Drives modules or removable Redundant Array of Independent  
 13 Disks modules may be installed to provide functionality. In  
 14 this same space removable interface/interconnectivity  
 15 modules contain embedded central processing units. Other  
 16 features may be placed to create a complete file server or  
 17 similar hardware requirements. The Redundant Array of  
 18 Independent Disks module provides for two removable, hot-

1 swappable controller cards as well as a universal I-O panel  
2 for either Small Computer System Interface host connectivity  
3 or Fiber Channel Arbitrated Loop host connectivity. The  
4 Just A Bunch of Drives module provides for redundant No  
5 Single Point of Failure I-O data paths and porting from a  
6 connected computer to an internal drive channels. Either  
7 the Just-A Bunch of Drives version or the Redundant Array of  
8 Independent Disks version has a removable interface feature  
9 that will serve any and all current and future bus  
10 technologies intended for data transfer.

11 Referring to Fig. 48 in conjunction with Fig. 46 and  
12 Fig. 49 the advanced technology back plane 414 is a  
13 connection point for all of the components that make up the  
14 peripheral enclosure 410. These components include fifteen  
15 storage device drives 411, blowers 413, controller cards 416  
16 and power supplies 415. Using board-to-board connectivity,  
17 there is a bus impedance matching to achieve maximum data  
18 transfer rates and reduce error rates. The power supplies

1 415, the blowers 413, the personality boards 417 and all of  
2 the storage devices 411 connect directly to the back-plane  
3 414 thereby reducing cabling and improving both  
4 functionality and reliability. The SAF-TE/SES, SCSI  
5 termination and other features are all built onto the back  
6 plane 414. There is a split bus in that the back plane 414  
7 may be divided into dual channels. A Small Computer System  
8 Interface Accessed Fault Tolerant Enclosure module may be  
9 plugged into the personality board 412 in order to provide  
10 the user with the ability to monitor all of the aspects of  
11 the enclosure. These aspects include cooling, power  
12 supplies and device activity from a remote computer system  
13 that is connected to the peripheral enclosure through a host  
14 or network connection. The module is designed to be  
15 universally plugged in it has two connections, one on each  
16 end. The module can be plugged in one way in order to  
17 enable it on the host channel or be flipped around the other  
18 way to enable it on the drive channel. The way the module

1 is plugged in determines how it will interface with the host  
2 system.

3 Referring to Fig. 49 in conjunction with Fig. 46, Fig.  
4 47 and Fig. 48 the first fifteen storage-device peripheral  
5 enclosure 410 is modular and fault-tolerant. There can be  
6 more than one power supply 415, more than one blower 413 and  
7 more than one controller card 416. If any one of the  
8 components fails the other one will continue to adequately  
9 run the peripheral enclosure 410. If a power supply 415  
10 fails out there is at least one power supply 415 still left.  
11 The remaining power supply 415 will provide enough power to  
12 run the peripheral enclosure 410. These modular components  
13 are hot-swappable so that the unit does not have to be shut  
14 down to replace the component. If a power supply 415 has  
15 failed it is simply replaced with a new one while the first  
16 ten storage-device peripheral enclosure 410 continues to run  
17 uninterrupted. The special connectors that are used on the  
18 power supplies 415 have specially designed pins that are



1 staggered and allow hot swapping. If the user decides to  
2 install two controller cards, they can be setup in a fault-  
3 tolerant configuration. This configuration provides that if  
4 one of the controller cards 416 fails, the other controller  
5 card 416 will continue to work without causing system  
6 downtime. The special connectors are used on the back plane  
7 414 and allow a failed controller card 416 to be replaced  
8 while the system continues to function.

9 Referring to Fig. 50 a second fifteen device peripheral  
10 enclosure 510 includes fifteen storage devices 511, fifteen  
11 canister 512, two blowers 513, a back plane 514, two power  
12 supplies 515 and two personality boards 517. The  
13 personality boards 517 are located in the back of the second  
14 fifteen-bay peripheral enclosure 510.

15 Referring to Fig. 51 in conjunction with Fig. 52, Fig.  
16 53 and Fig. 54 a third fifteen device peripheral enclosure  
17 610 includes fifteen storage devices 611, fifteen canisters  
18 612, two blowers 613, a back plane 614, two power supplies

1 615, two personality boards 617 and two carriers 618. Each  
2 of the two personality boards 617 is disposed in one of the  
3 two carriers 618. The carriers 618 are located in the back  
4 of the third fifteen storage-device peripheral enclosure  
5 610. The personality boards 617 plug into the back plane  
6 614. The personality boards 617 are connected to the two  
7 power supplies 615 and SAF-TE/SES. The personality boards  
8 617 are also connected to the storage devices 611 that are  
9 connected to the opposite side of the back plane 614. The  
10 storage devices 611 of the third fifteen storage-device  
11 peripheral enclosure 610 may include an array of fifteen  
12 Small Computer System Interface storage disk drives 611 that  
13 have a low voltage differential. The third fifteen storage-  
14 device peripheral enclosure 610 may be used in either Just-A  
15 Bunch of Drives applications or Redundant Array of  
16 Independent Disks applications.

17 Referring to Fig. 55 in conjunction with Fig. 56 the  
18 third rack-mountable fifteen storage-device peripheral

1 enclosure 610 also includes dual personality boards 617, two  
2 carriers 618 for the dual personality boards 618 and a  
3 container 619 for the two carriers 618.

4 Referring to Fig. 57 in conjunction with Fig. 58 a  
5 fourth rack-mountable fifteen storage-device peripheral  
6 enclosure includes casing 710, a back plane 714 and a  
7 container 719 that a user may insert his own controller  
8 cards and personality boards.

9 Referring to Fig. 59 in conjunction with Fig. 60 and  
10 Fig. 61 a fifth rack-mountable fifteen storage-device  
11 peripheral enclosure includes casing 810, a back plane 814,  
12 dual personality boards 817 and two carriers 818 for the  
13 dual personality boards 817.

14 Referring to Fig. 62 in conjunction with Fig. 63 a  
15 sixth rack-mountable fifteen storage-device peripheral  
16 enclosure includes casing 910, fifteen storage device 911,  
17 fifteen canister 912, two blowers 913, a back plane 914, two  
18 power supplies 915 and dual personality boards 917.

1 Referring to Fig. 64 in conjunction with Fig. 65 and  
 2 Fig. 66 a seventh rack-mountable fifteen storage-device  
 3 peripheral enclosure 1010 includes casing, fifteen storage  
 4 devices 1011 and fifteen canisters 1012. The seventh rack-  
 5 mountable fifteen storage-device peripheral enclosure 1010  
 6 also includes two blowers 1013, a back plane 1014, two power  
 7 supplies 1015 and a circuit board 1116 and a container 1019  
 8 that a user may insert his own controller cards and  
 9 personality boards.

10 Referring to Fig. 67 in conjunction with Fig. 68 and  
 11 Fig. 69 an eighth rack-mountable fifteen storage-device  
 12 peripheral enclosure 1110 includes casing, light pipes 1111  
 13 and handles 1112 of fifteen canisters and a back plane 1114.

14 Referring to Fig. 70 in conjunction with Fig. 69 the  
 15 back plane has a connector. A canister 1220 holds a storage  
 16 device 1221 and an electrical converter 1222. The  
 17 electrical converter 1222 connects to the connector of the  
 18 back plane.

1        From the foregoing it can be seen that a peripheral  
2        computer enclosure have been described.

3           Accordingly it is intended that the foregoing  
4   disclosure be considered only as an illustration of the  
5   principle of the present invention.

[illegible]